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Sheet	1	of	2
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Complete if Known

Application Number	10/511,575
Filing Date	10/13/2004
First Named Inventor	Uwe Porst
Art Unit	Not yet assigned
Examiner Name	Not yet assigned
Attorney Docket Number	A36393-PCT-USA (066340.0212)

U. S. PATENT DOCUMENTS

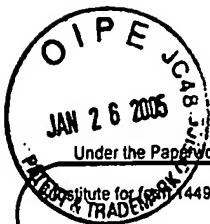
FOREIGN PATENT DOCUMENTS

Examiner Signature	/Keith Vicary/	Date Considered	10/03/2007
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PTO/SB/08B (08-03)

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Sheet 1

of 2

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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
/KV/	2	Wichman S. et al., "The Second Generation ZSP DSP," LSI Logic Corp., In: Bank Systems & Technology Techlibrary: Digital Signal Processors, 01.01.02, p. 1-8	
/KV/	3	Mukherjee, M. et al., "Disabling the Internal Oscillator with External Crystal on the TMS320VC5509 DSP," Application Report SPRA078, April 2002, Texas Instruments, p. 1-12	
/KV/	4	Richter et al., "A Platform-Based Highly Parallel Digital Signal Processor," Proceedings of the IEEE 2001 Custom Integrated Circuits Conference, San Diego, California, May 6-9, 2001, p. 13-6-1 - 13-6-4	

Examiner Signature	/Keith Vicary/	Date Considered	10/03/2007
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